ABSTRACT OF THE DISCLOSURE

An electronic animal trap with a CPU-controlled, rearming, high voltage output circuit for trapping and exterminating pests. The high-voltage output circuit is connected to a pair of killing plates which are activated with a high-voltage pulse train for a period of time constituting a killing cycle when a known impedance is sensed across the plates. If, upon conclusion of the killing cycle, the impedance remains across the plates, the circuit activates a visual indicator to alert a user that the trap contains a dead mouse. However, if the impedance increases to that of an open circuit, likely meaning the animal has escaped, the circuit automatically rearms itself and no visual indicator is initiated.